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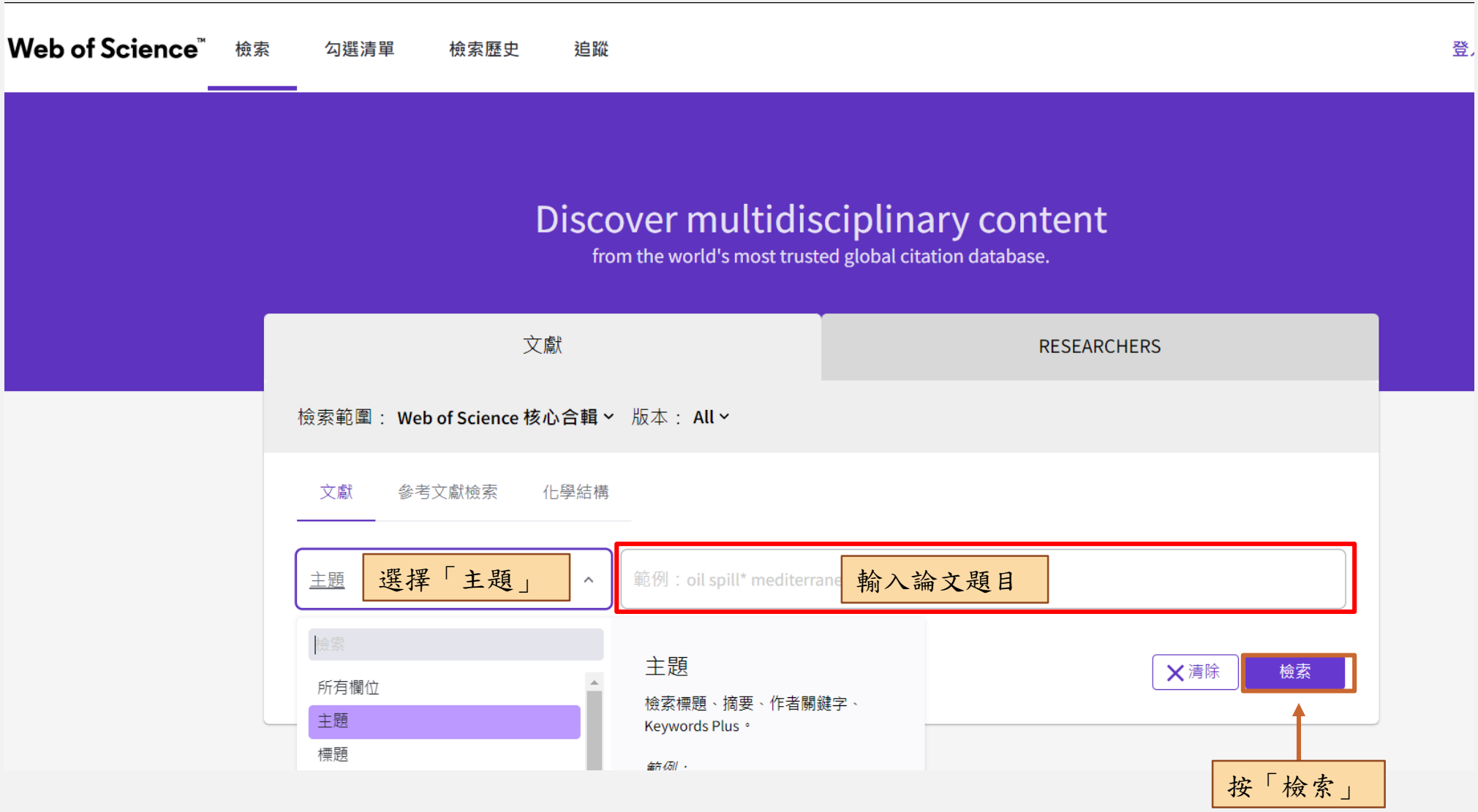

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Lee, YL; Chang, SJ; (...); Cheng, YP

May 2017 | IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS II-EXPRESS BRIEFS 64 (5) , pp.500-504

A continuous-rate clock and data recovery (CDR) circuit with unbounded frequency detection mechanism is proposed herein. The unbounded frequency detection mechanism combines the digital quadricorrelator frequency detection and subharmonic tone frequency detection techniques. By adopting the unbounded frequency detector, this reference-less CDR circuit has no locking range limitation and provide ... 顯示更多

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By Lee, YL (Lee, Yen-Long) [1] ; Chang, SJ (Chang, Soon-Jyh) [1] ; Chen, YC (Chen, Yen-Chi) [1] ; Cheng, YP (Cheng, Yu-Po) [1]
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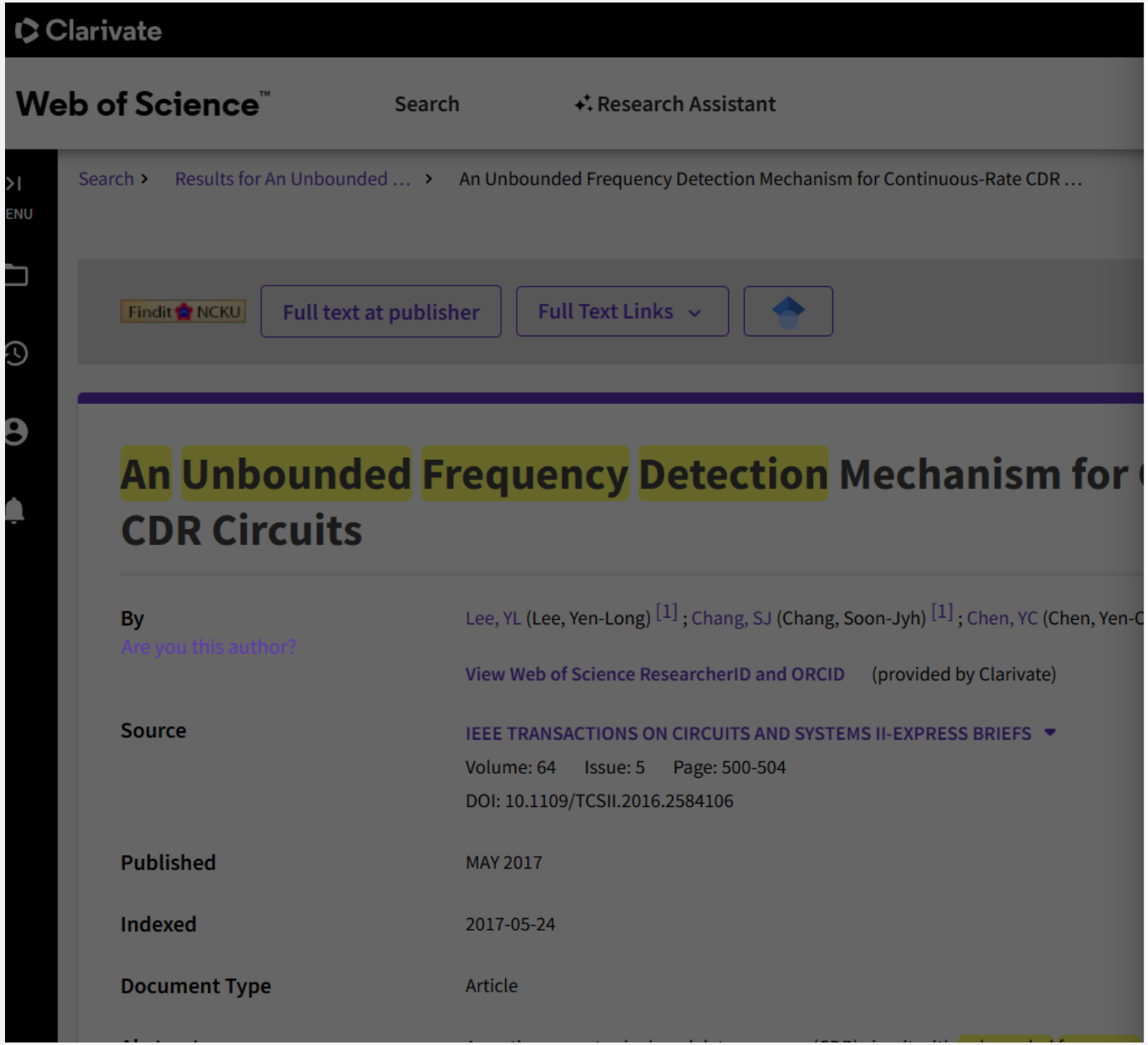

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